# **CPL64** 2-CH PCI Express<sup>®</sup> PoCL Frame Grabber







## Features

- PCI Express<sup>®</sup> x4 compliant
- Supports 2-CH Camera Link<sup>®</sup> Base configuration
- Acquisition pixel clock rates up to 85 MHz
- PoCL (Power over Camera Link<sup>®</sup>) safe power compliant with auto detection
- 128 MB of 200 MHz DDR SDRAM for acquisition
- 4 TTL Digital Input/Output, and 2 trigger Input
- Supports 64-bit memory addressing
- Two serial communication ports

## Applications

- PCB/FPD/Wafer/Solar Cell surface inspections
- Medical research instrumentations

### Software Support

#### Windows® Platform

- Available for Windows  $^{\ensuremath{\mathbb{R}}}$  Vista (64/32-bit)/XP
- CamCreator<sup>™</sup>
  - CamCreator assists developers in quickly evaluating initial tests and functions.

#### Ordering Information

- CPL64
  - 2-CH PCI Express<sup>®</sup> x4 PoCL frame grabber

#### Accessories

- Cabling
- PoCL Cable
  - 5 M, power over Camera Link cable
- Camera Link Cable
  - 5 M, robot type

## Overview

CPL64 is a PoCL (Power over Camera Link<sup>®</sup>) frame grabber that is based on the PCI Express<sup>®</sup> x4 interface, and supports two-channel Camera Link "base" configurations, multi-tap area and line scan cameras. The CPL64 frame grabber strikes a perfect balance between performance and cost. It is capable of simultaneously image acquisition from two completely independent Camera Link base configuration cameras, and supports image transfers rates up to 512 MB/s.

#### **PoCL Technology**

The PoCL (Power over Camera Link<sup>®</sup>) standard allows the camera link cable to supply power to the camera through the Camera Link connector without losing backward compatibility with the previous Camera Link<sup>®</sup> standard, this solution is particularly suitable for a small camera.

#### **Benefits of PoCL**

- Easy installation
- Reduce wiring (Single cable for digital I/F, and power)
- Reduce camera size

## Specifications

Form Factor	PCI Express <sup>®</sup> x4 compliant
Video Input	Camera Link <sup>®</sup> LVDS deferential signals
	Dual Base Configuration: Using two MDR26 pins connectors
	Maximum camera link data rate: 85 MHz
	Supports PoCL and standard Camera Link interface and
	auto detect
Camera Control	LVDS camera control: CC1 to CC4 control signal in two
	MDR26 pins connectors
External Signal Input	External RS422 level A, B, Z phase deferential signal for
	encoder input
	2 channels TTL level Line /Area trigger input
	2 channels TTL level Line trigger start input
	2 channels TTL level exposure output
	Line trigger bypass output (encoder mode only)
	4 channels digital input; 4 channels digital output
Power over Camera Link <sup>®</sup> (PoCL)	Power line output per channel : DC + I2 V max @ I A
	Over-current Protection function, auto detect non-PoCL
	cable or PoCL camera connected.
Operating Environment	Temperature: $0^{\circ}$ C to $+50^{\circ}$ C
	Humidity: 5% to 90% RHNC
Storage Environment	Temperature: 0°C to +70°C
	Humidity: 0 to 95% RHNC
Power Requirements	+12 V max @ 0.5 A
	+3.3 V max @ 1.6 A

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